1. 3 Watt Audio Power Amplifier

Features

- Improved PSRR at 217 Hz 68 dB
- Power output at 5.0V, 1% THD+N, 8Ω 1.3 W (typ.)
- Power output at 3.0V, 1% THD+N, 8Ω 480 mW (typ.)
- Ultra low shutdown current 0.1 uA (typ.)
- 2.2V – 5.5V operation
- Improved circuitry eliminates pop-click noise during turn-on and turn-off transitions
- Excellent RFI (Radio Frequency Interference) immunity
- No output coupling capacitors, snubber networks or bootstrap capacitors required
- Unity-gain stable
- External gain configuration capability
- Available in space-saving packages: CSP9

General Description

The BL6212 is a Class-AB audio power amplifier designed for mobile phones and other portable communication devices. It is capable of delivering 1.3 watts of continuous average power to an 8Ω BTL load with less than 1% distortion (THD+N) from a $5V_{DC}$ power supply.

The BL6212 was designed specifically to provide high quality output power with a minimal amount of external components. It does not require output coupling capacitors or bootstrap capacitors. And with ultra low shutdown current, the BL6212 is ideally suited for mobile phone and other low voltage applications where minimal power consumption is a primary requirement.

With special pop-click eliminating circuit, the BL6212 provides perfect pop-click characteristic during turn-on and turn-off transitions.

The BL6212 is unity-gain stable and can be configured by external gain-setting resistors.

Applications

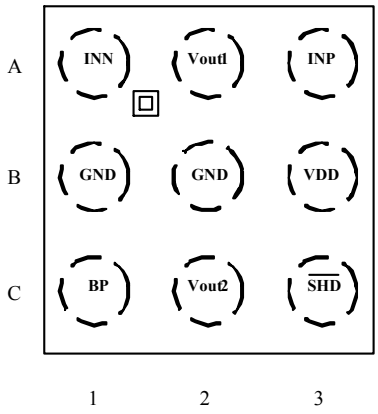
- Wireless handsets
- Portable electronic devices
- PDAs, Handheld computers

Order Information

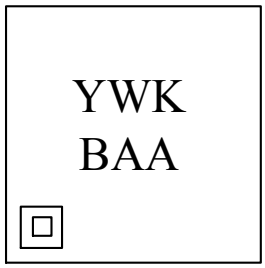
Part Number	Package	Shipping
BL6212CP	9 Bump WLCSP	3000 pcs / Tape & Reel

Pin Diagrams

9 Bump WLCSP Package
(Top View)



9 Bump WLCSP Marking
(Top View)



Y – Year Code
WK - Week Code
BAA - BL6212V2 9-BUMP ROHS

Pin Description

No.	Pin Name	I/O	Description
A1	INN	I	Negative Input
A2	Vout1	O	Negative BTL Output
A3	INP	I	Positive Input
B1/B2	GND	I/O	Ground
B3	VDD	I/O	Power Supply (2.2 – 5.5 V)
C1	BP	I/O	Analog ground for inner OPAs. It's about a half of VDD.
C2	Vout2	O	Positive BTL Output
C3	$\overline{SHD}$	I	Shout-down Logical Control, '0' is active.

Typical Application Circuit

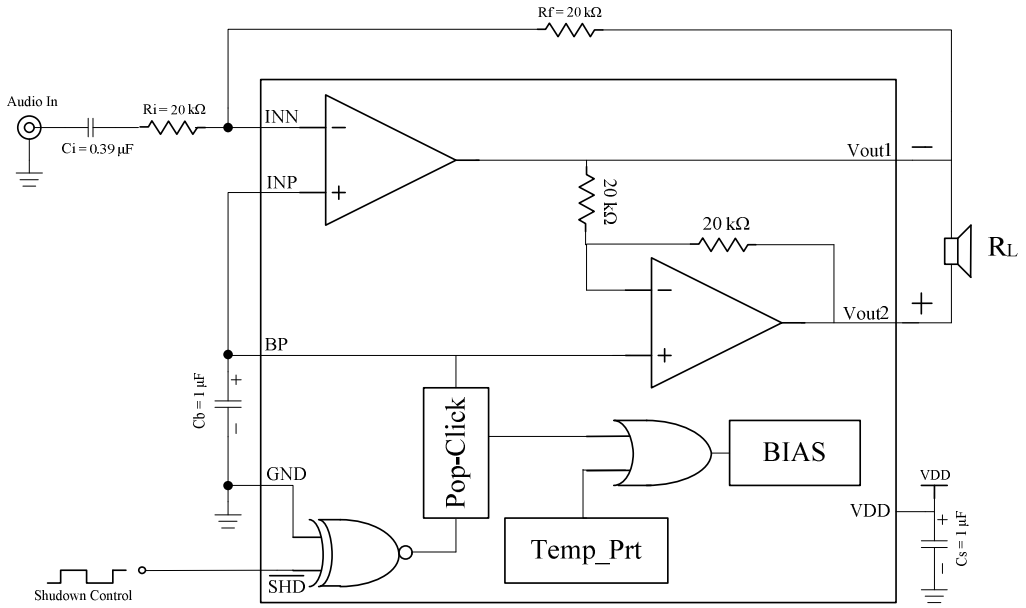


FIGURE 1. BL6212 Typical Application Circuit

External Components Description

Components	Functional Description
Ri	Inverting input resistance which sets the closed-loop gain in conjunction with Rf. This resistor also forms a high pass filter with Ci at fc = 1/(2πRi*Ci).
Ci	Input coupling capacitor which blocks the DC voltage at the amplifiers input terminates. Also creates a high-pass filter with Ri at fc = 1/(2πRi*Ci).
Rf	Feedback resistance which sets the closed-loop gain in conjunction with Ri.
Cs	Supply bypass capacitor which provides power supply filtering.
Cb	Bypass pin capacitor which provides half-supply filtering. Refer to the section.

Absolute Maximum Ratings

Supply Voltage -0.3V to 6V
Input Voltage -0.3V to VDD+0.3V
Power Dissipation
See Dissipation Rating Table
Junction Temperature -40℃ to +150℃
Storage Temperature -65℃ to +150℃
Thermal Resistance
θ_{JA}(9-BUMP) 90℃/W

Operating Ratings

Temperature Range -40℃ ≦ T_A ≦ 85℃
Supply Voltage 2.2V ≦ V_{DD} ≦ 5. 5V

NOTE: Absolute Maximum Ratings indicate limits beond which damage to the device may occur. Operating Rating indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Electrical Characteristics

The following specifications apply for the circuit shown in Figure 1, unless otherwise specified. Limits apply for T_A = 25℃.

❑ **V_{DD} = 5V**

Symbol	Parameter	Conditions	Spec			Units
			Min.	Typ.	Max.	
I_{DD}	Quiescent Power Supply Current	V_{IN} = 0V, 8Ω Load		3.6	8	mA
		V_{IN} = 0V, No Load		3.3	7	mA
I_{SD}	Shutdown Current	V_{IN}=0V, V_{SHD}=GND, No Load		0.1	2	uA
V_{SDIH}	Shutdown Voltage Input High			0.88		V
V_{SDIL}	Shutdown Voltage Input Low			0.73		V
V_{OS}	Output Offset Voltage		-50	6	50	mV
THD+N	Total Harmonic Distortion+Noise	Po=0.5Wrms, f=1KHz,		0.13		%
P_O	Output Power	THD+N<=1%, f=1KHz, 8Ω Load	0.9	1.30		W
PSRR	Power Supply Rejection Ratio	Input terminated with 10Ω, $V_{DDRIPPLE}$=0.2V_{P-P}, f=217Hz	55	68		dB
		Input terminated with 10Ω, $V_{DDRIPPLE}$=0.2V_{P-P}, f=1KHz	55	63		dB
T_{WU}	Wake-up time			106		ms
R_{OUT}	Resistor Output to GND			8.3		kΩ

❑ **V_{DD} = 3V**

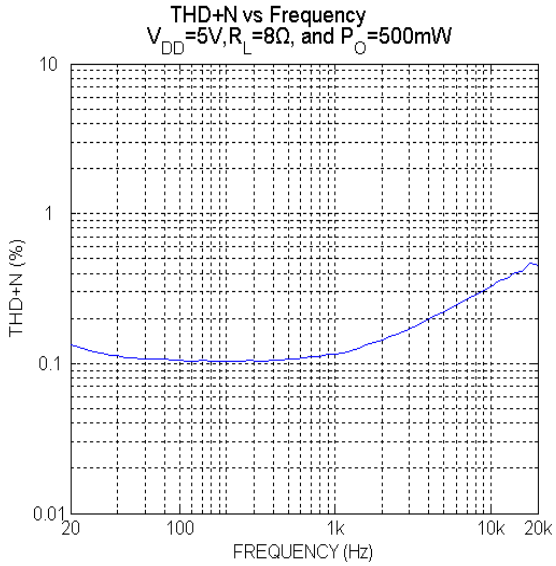
Symbol	Parameter	Conditions	Spec			Units
			Min.	Typ.	Max.	
I_{DD}	Quiescent Power Supply Current	V_{IN} = 0V, 8Ω Load		3.0	7	mA
		V_{IN} = 0V, No Load		2.6	6	mA
I_{SD}	Shutdown Current	V_{IN}=0V, V_{SHD}=GND, No Load		0.1	2	uA
V_{SDIH}	Shutdown Voltage Input High			0.80		V
V_{SDIL}	Shutdown Voltage Input Low			0.67		V
V_{OS}	Output Offset Voltage		-50	6	50	mV
THD+N	Total Harmonic	Po=0.25Wrms, f=1KHz,		0.10		%

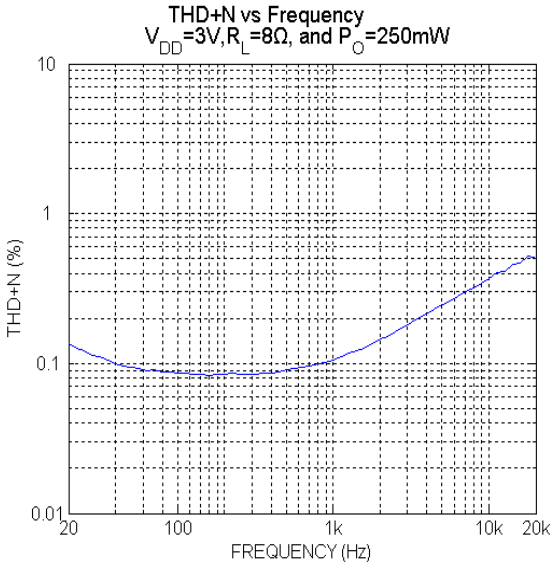
	Distortion+Noise					
P_O	Output Power	THD+N<=1%, f=1KHz, 8Ω Load		480		mW
PSRR	Power Supply Rejection Ratio	Input terminated with 10Ω, $V_{DDRIPPLE}$=0.2V_{P-P}, f=217Hz	55	68		dB
		Input terminated with 10Ω, $V_{DDRIPPLE}$=0.2V_{P-P}, f=1KHz	55	63		dB
T_{WU}	Wake-up time			84		ms
R_{OUT}	Resistor Output to GND			8.3		kΩ

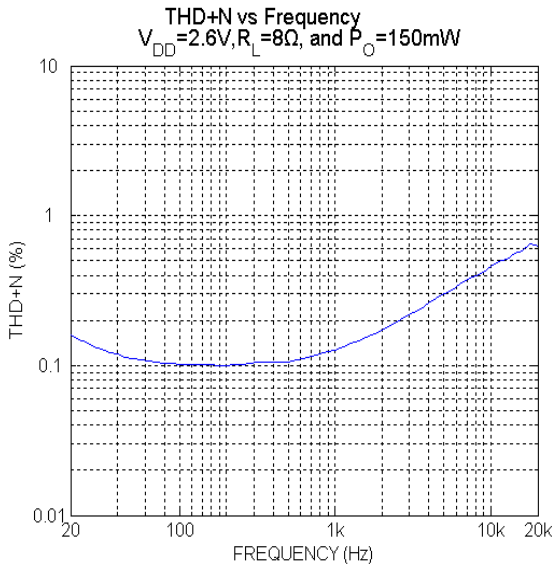
❑ **V_{DD} = 2.6V**

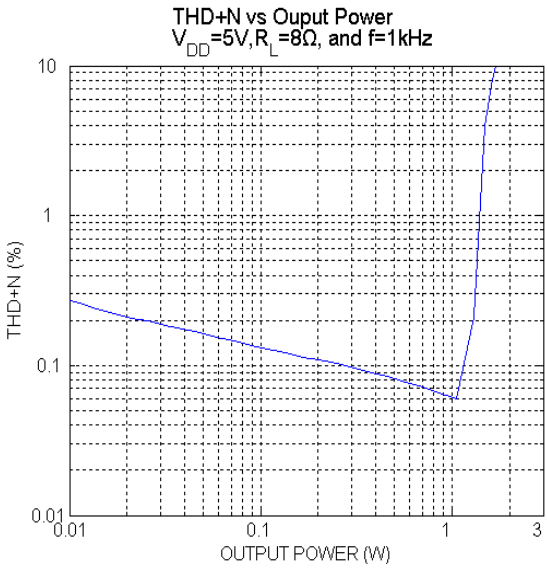
Symbol	Parameter	Conditions	Spec			Units
			Min.	Typ.	Max.	
I_{DD}	Quiescent Power Supply Current	V_{IN} = 0V, 8Ω Load		2.7		mA
		V_{IN} = 0V, No Load		2.5		mA
I_{SD}	Shutdown Current	V_{IN}=0V, V_{SHD}=GND, No Load		0.1		uA
V_{OS}	Output Offset Voltage		-50	4	50	mV
THD+N	Total Harmonic Distortion+Noise	Po=0.15Wrms, f=1KHz,		0.12		%
P_O	Output Power	THD+N<=1%, f=1KHz, 8Ω Load		365		mW
PSRR	Power Supply Rejection Ratio	Input terminated with 10Ω, $V_{DDRIPPLE}$=0.2V_{P-P}, f=217Hz	55	67		dB
		Input terminated with 10Ω, $V_{DDRIPPLE}$=0.2V_{P-P}, f=1KHz	55	62		dB
T_{WU}	Wake-up time			80		ms
R_{OUT}	Resistor Output to GND			8.3		kΩ

Typical Performance Characteristics

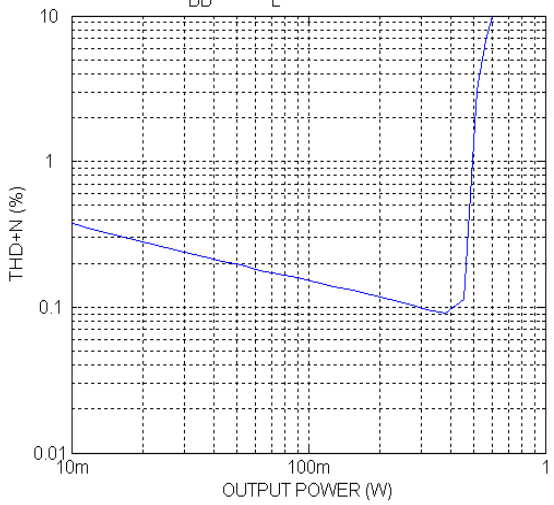




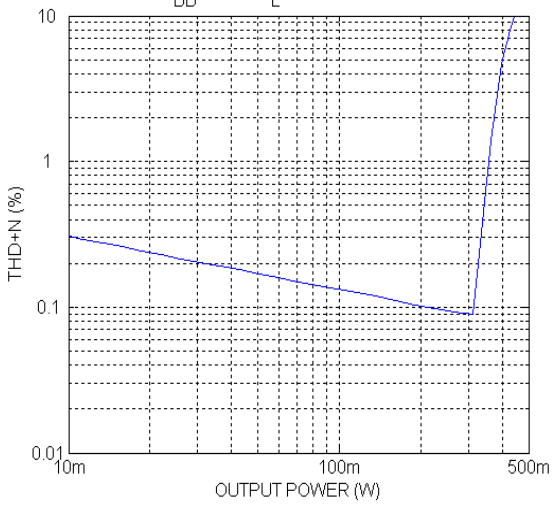




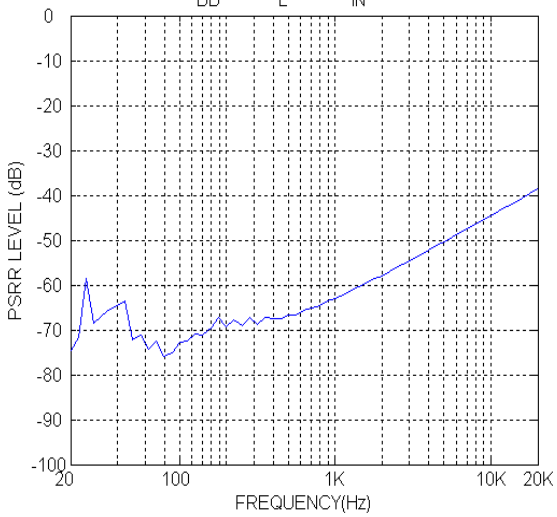
THD+N vs Ouput Power
V_{DD}=3V,R_L=8Ω, and f=1kHz



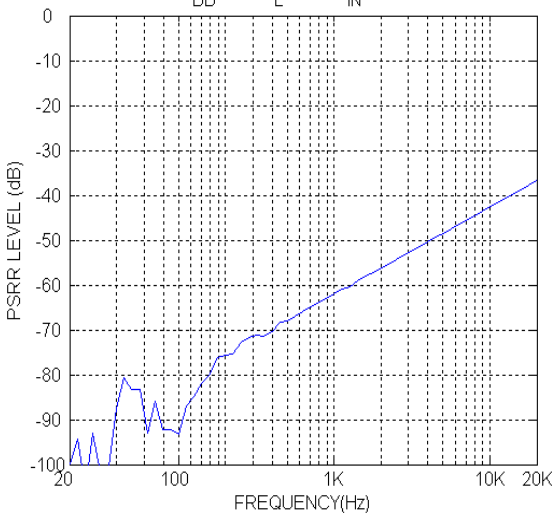
THD+N vs Ouput Power
V_{DD}=2.6V,R_L=8Ω, and f=1kHz



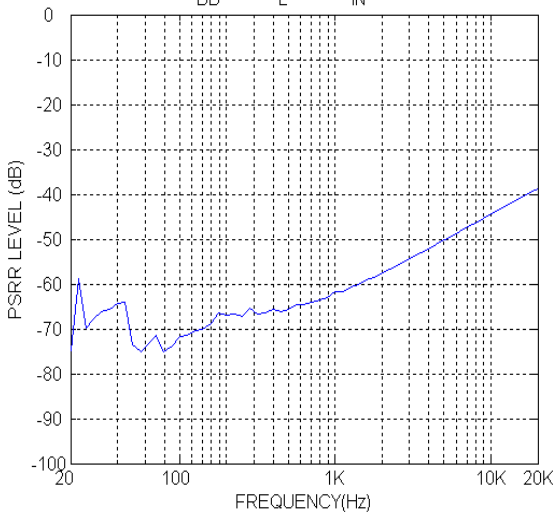
PSRR vs Frequency
V_{DD}=5V,R_L=8Ω, R_{IN}=10Ω



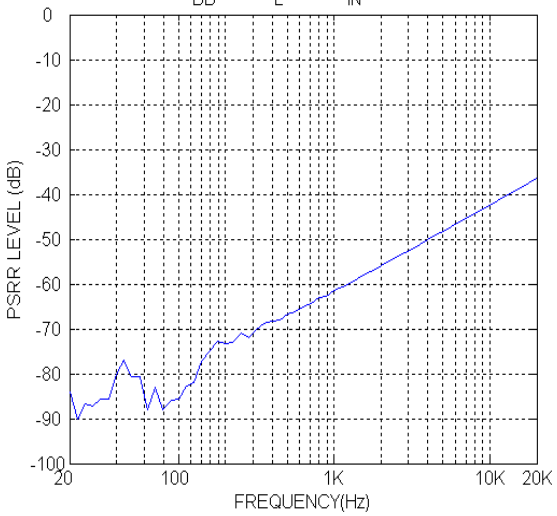
PSRR vs Frequency
V_{DD}=5V,R_L=8Ω, R_{IN}=Float



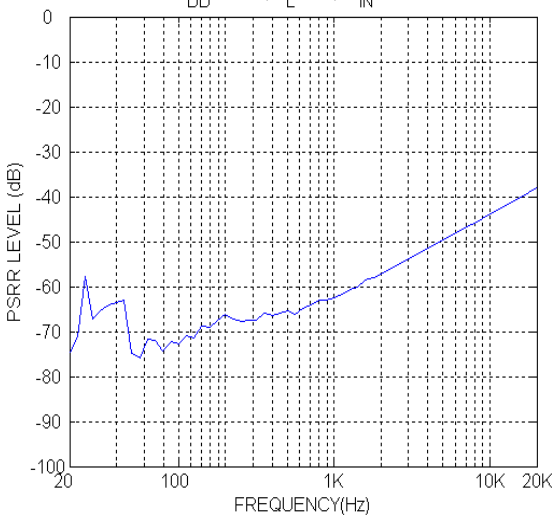
PSRR vs Frequency
V_{DD}=3V,R_L=8Ω, R_{IN}=10Ω



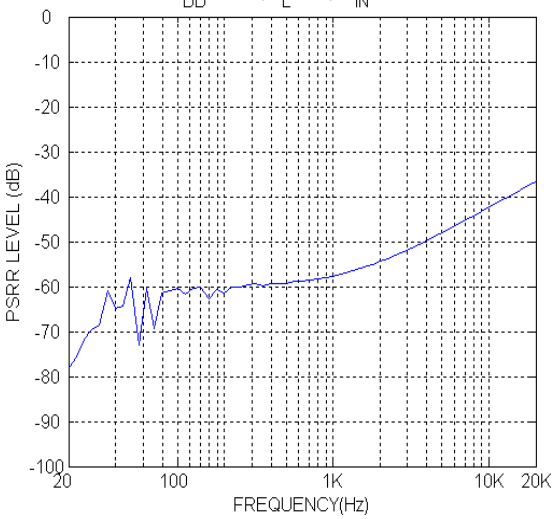
PSRR vs Frequency
V_{DD}=3V,R_L=8Ω, R_{IN}=Float



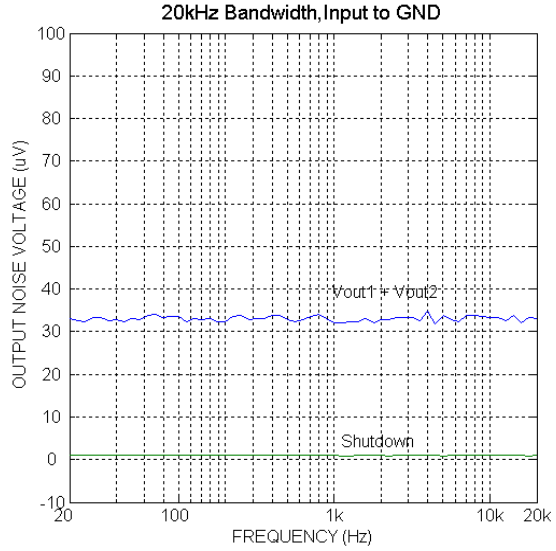
PSRR vs Frequency
V_{DD}=2.6V,R_L=8Ω, R_{IN}=10Ω



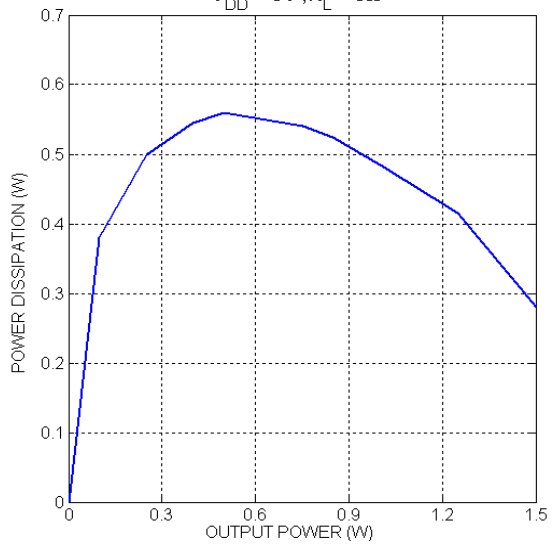
PSRR vs Frequency
V_{DD}=2.6V,R_L=8Ω, R_{IN}=Float



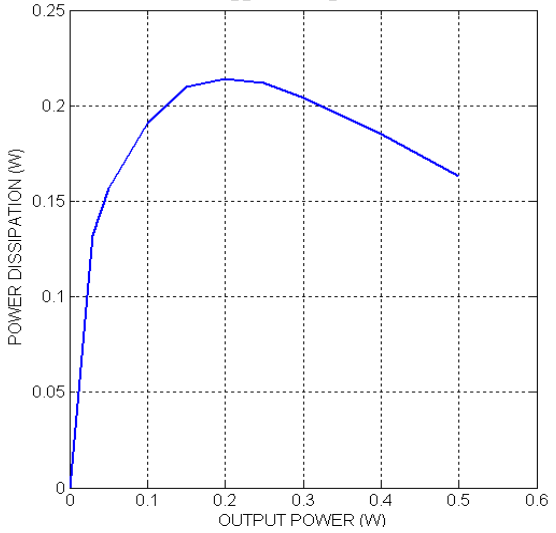
Noise Floor, 5V, 8Ω
20kHz Bandwidth,Input to GND



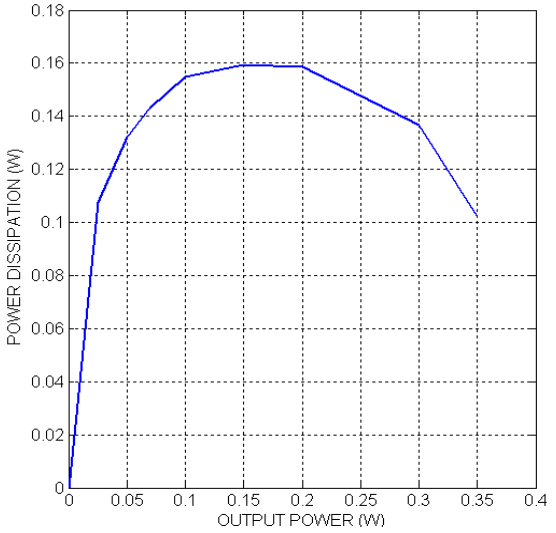
Power Dissipation vs Output Power
V_{DD} = 5V ,R_L = 8Ω

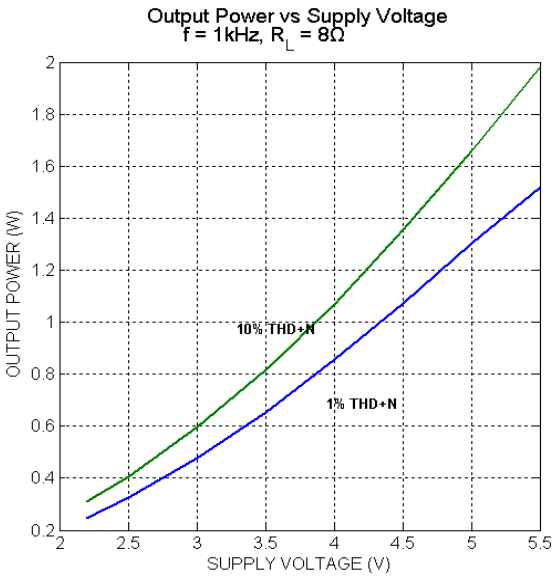


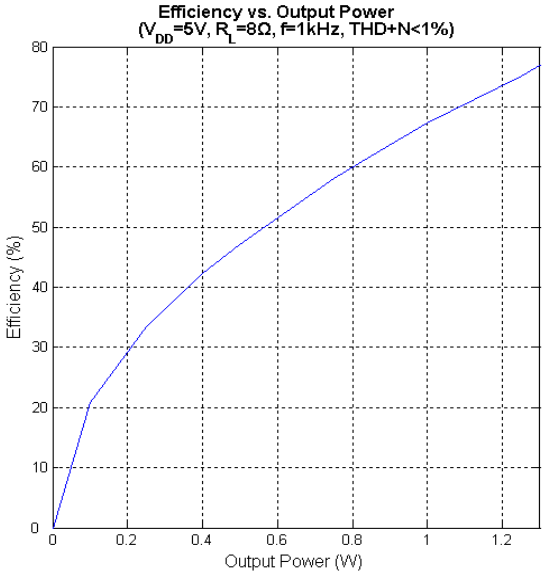
Power Dissipation vs Output Power
V_{DD} = 3V ,R_L = 8Ω



Power Dissipation vs Output Power
V_{DD} = 2.6V,R_L = 8Ω

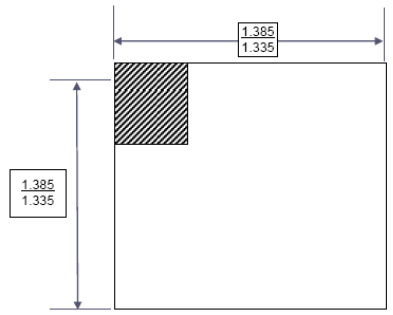




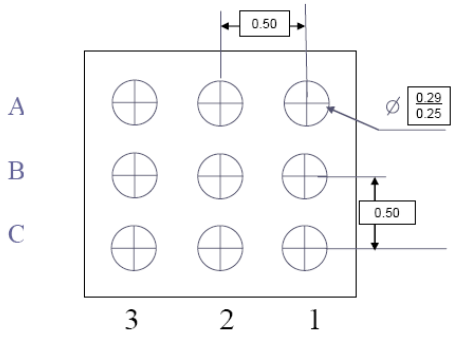


Package Dimensions

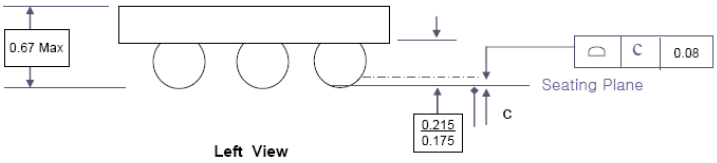
WLCSP 9 PACKAGE



Top View



Bottom View



Left View

NOTES: All linear dimensions are in millimeters.

A1 is the location for Pin 1